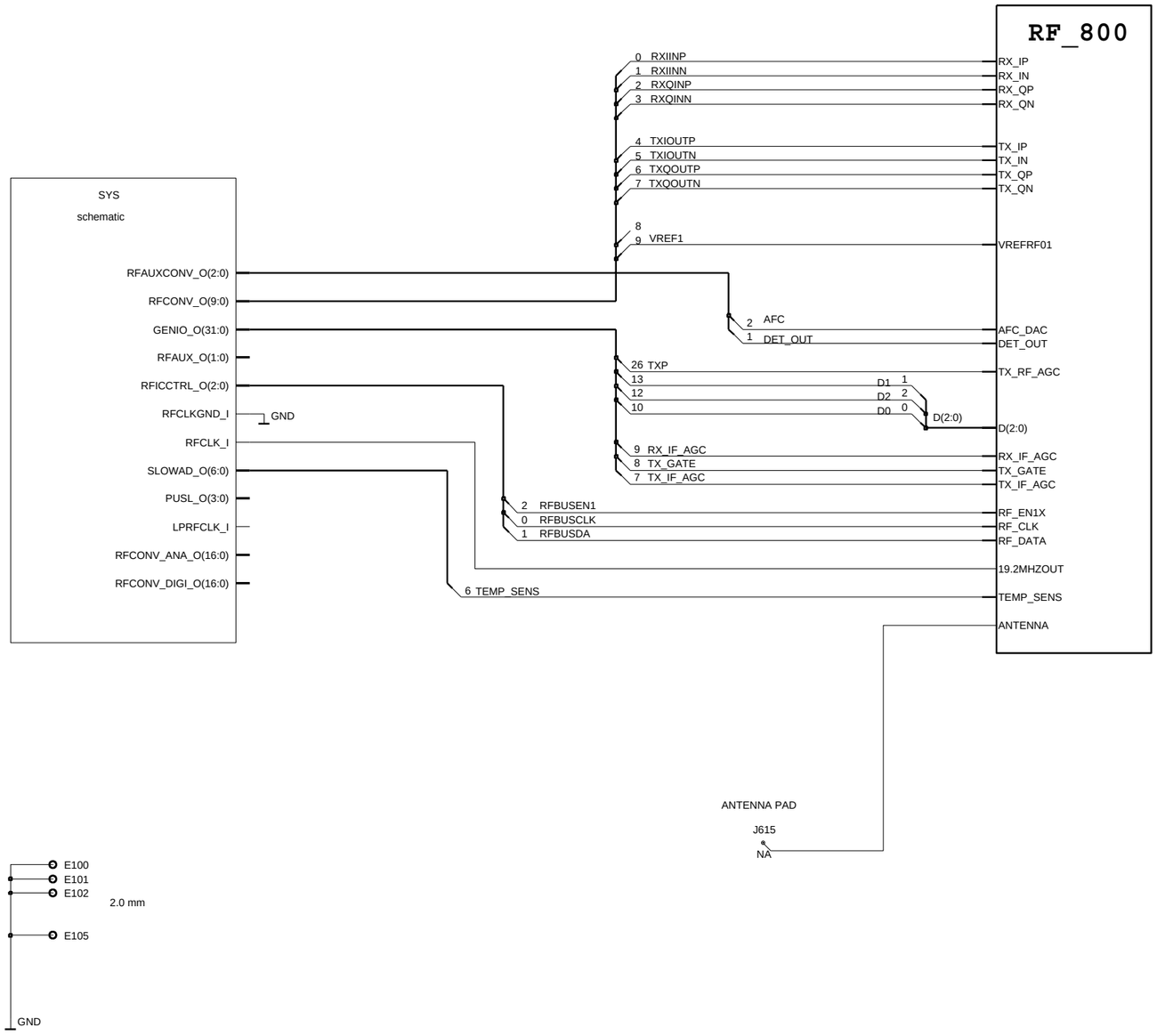
Top-Level Diagram



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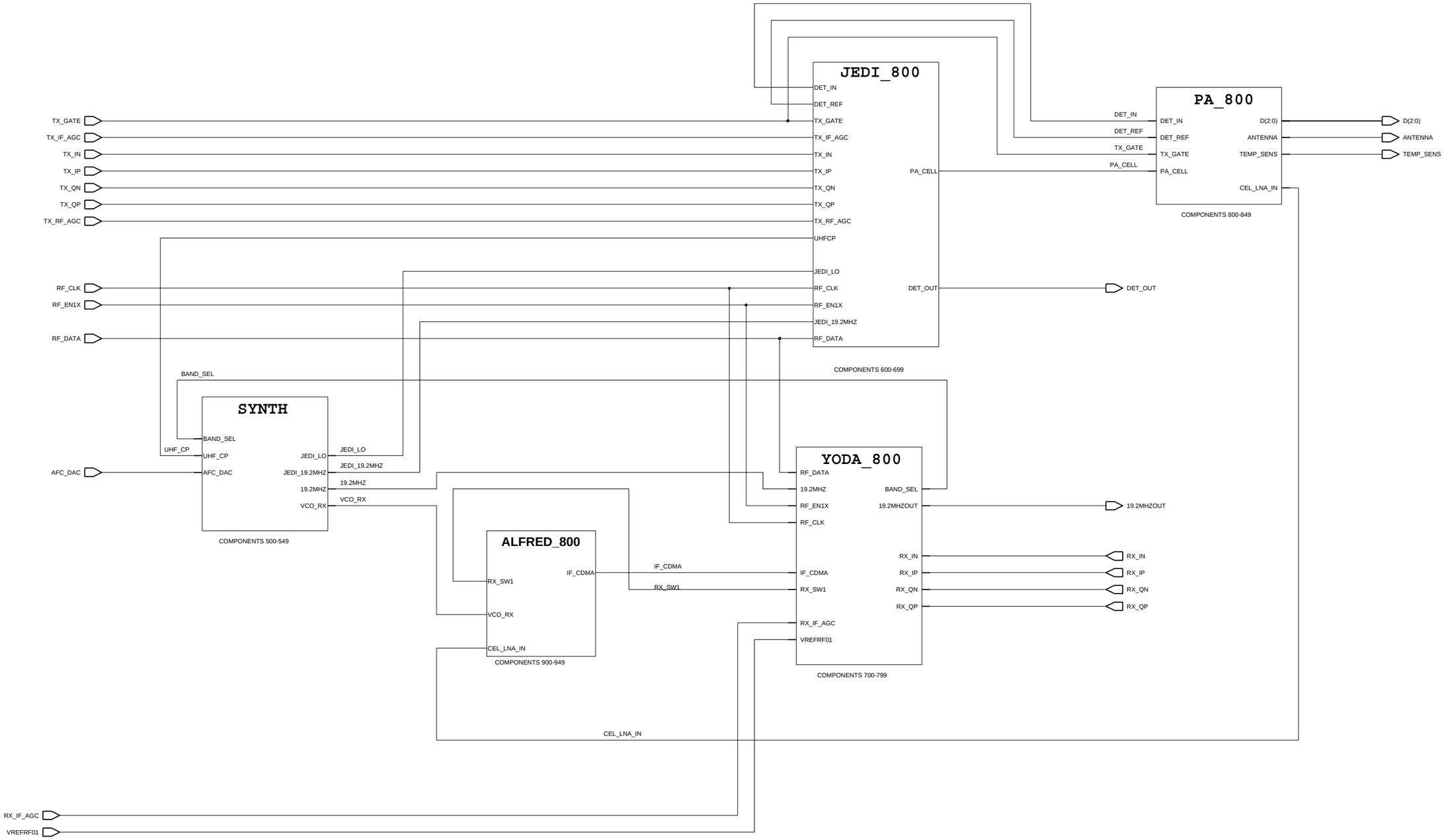
RF Top Level



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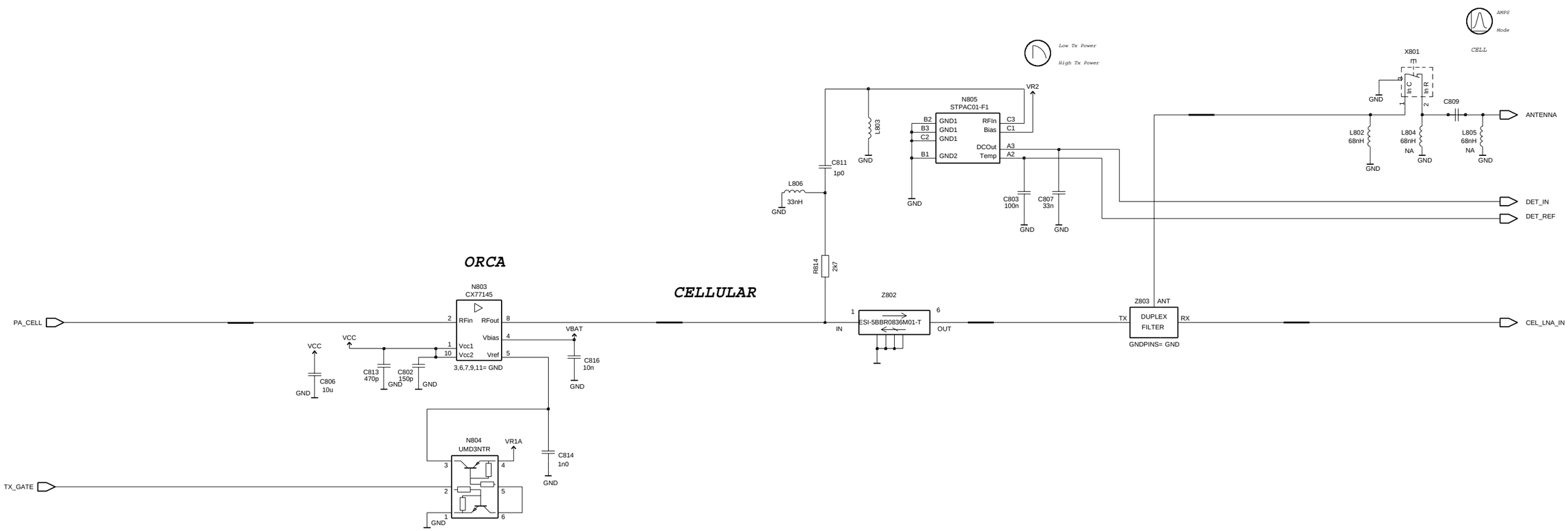
PA



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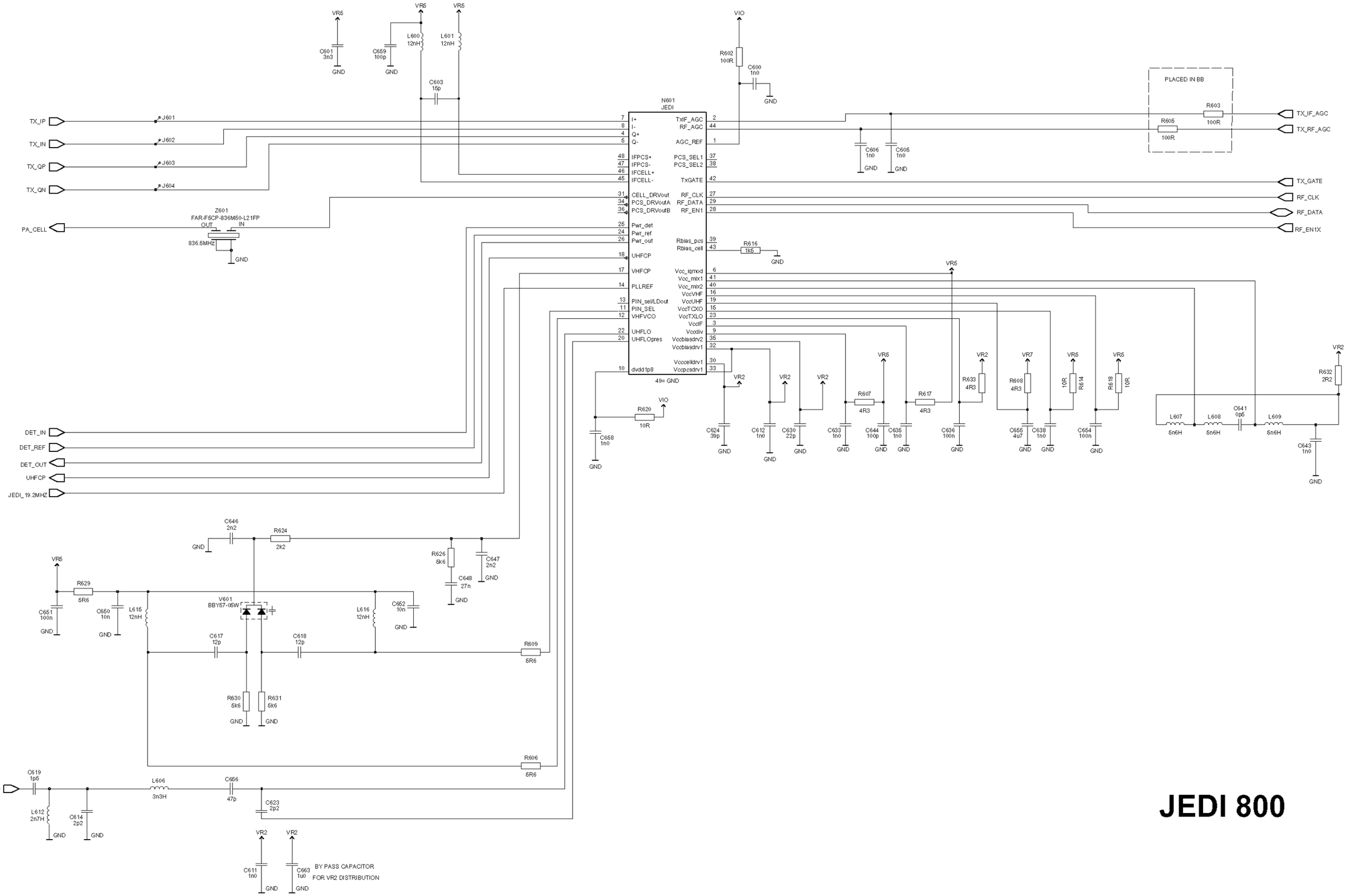
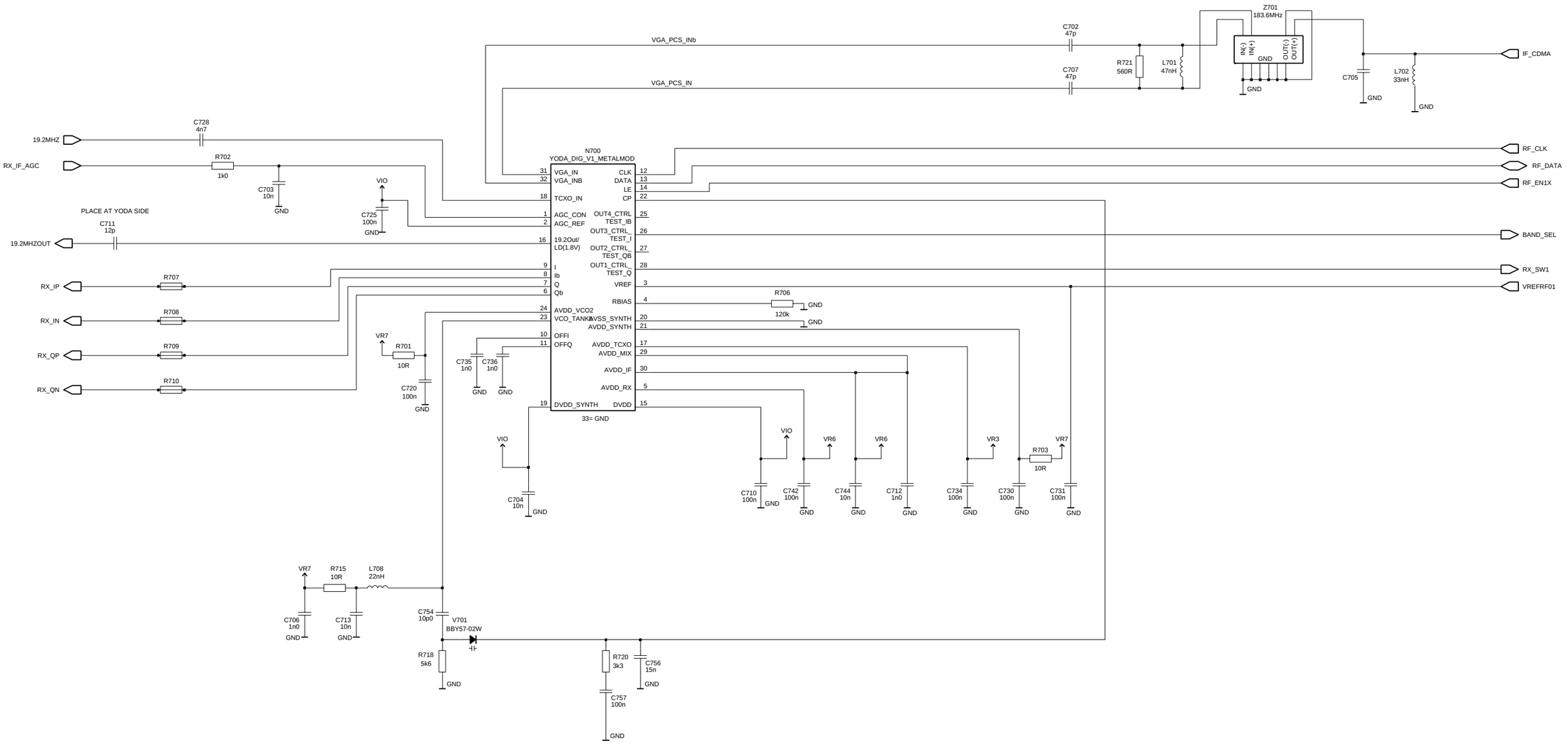
Jedi


N601
JEDI
TX_IP
TX_IN
TX_QP
TX_QN
PA_CELL
DET_IN
DET_REF
DET_OUT
UHFCP
JEDI_19.2MHZ
JEDI_LO
Z601
FAR-F5CP-836M50-L21FP
836.5MHz
V601
BBY57-05W
PLACED IN BB
TX_IF_AGC
TX_RF_AGC
TX_GATE
RF_CLK
RF_DATA
RF_EN1X
BY PASS CAPACITOR
FOR VR2 DISTRIBUTION
JEDI 800

Yoda



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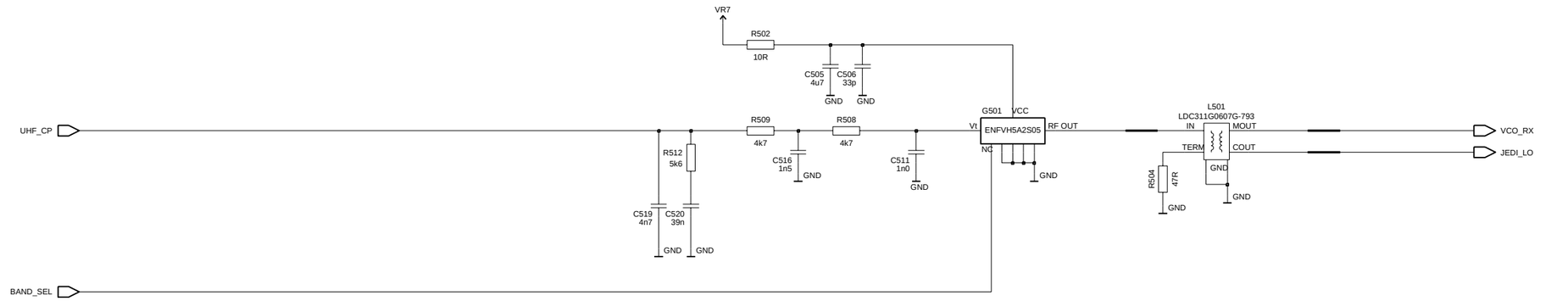
Synthesizer



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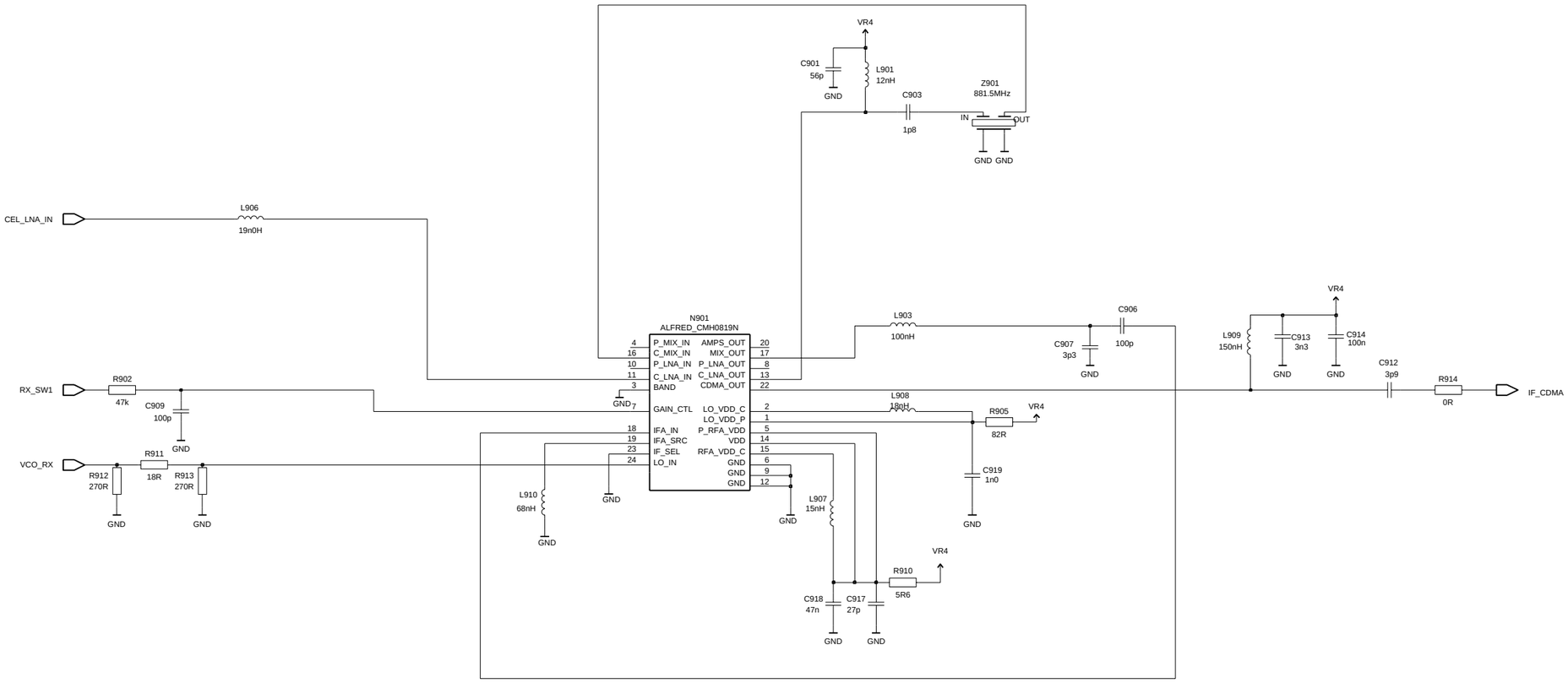
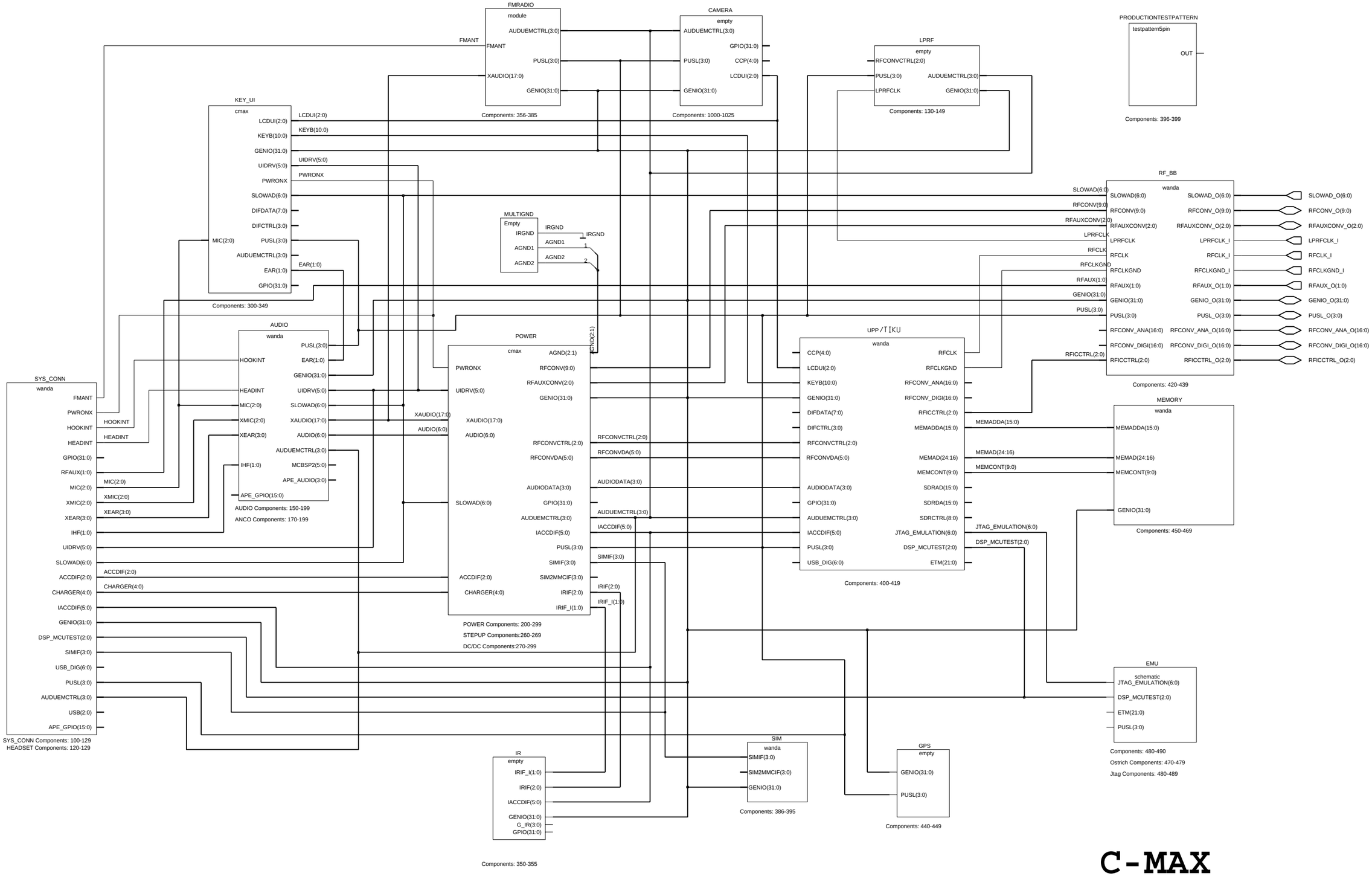
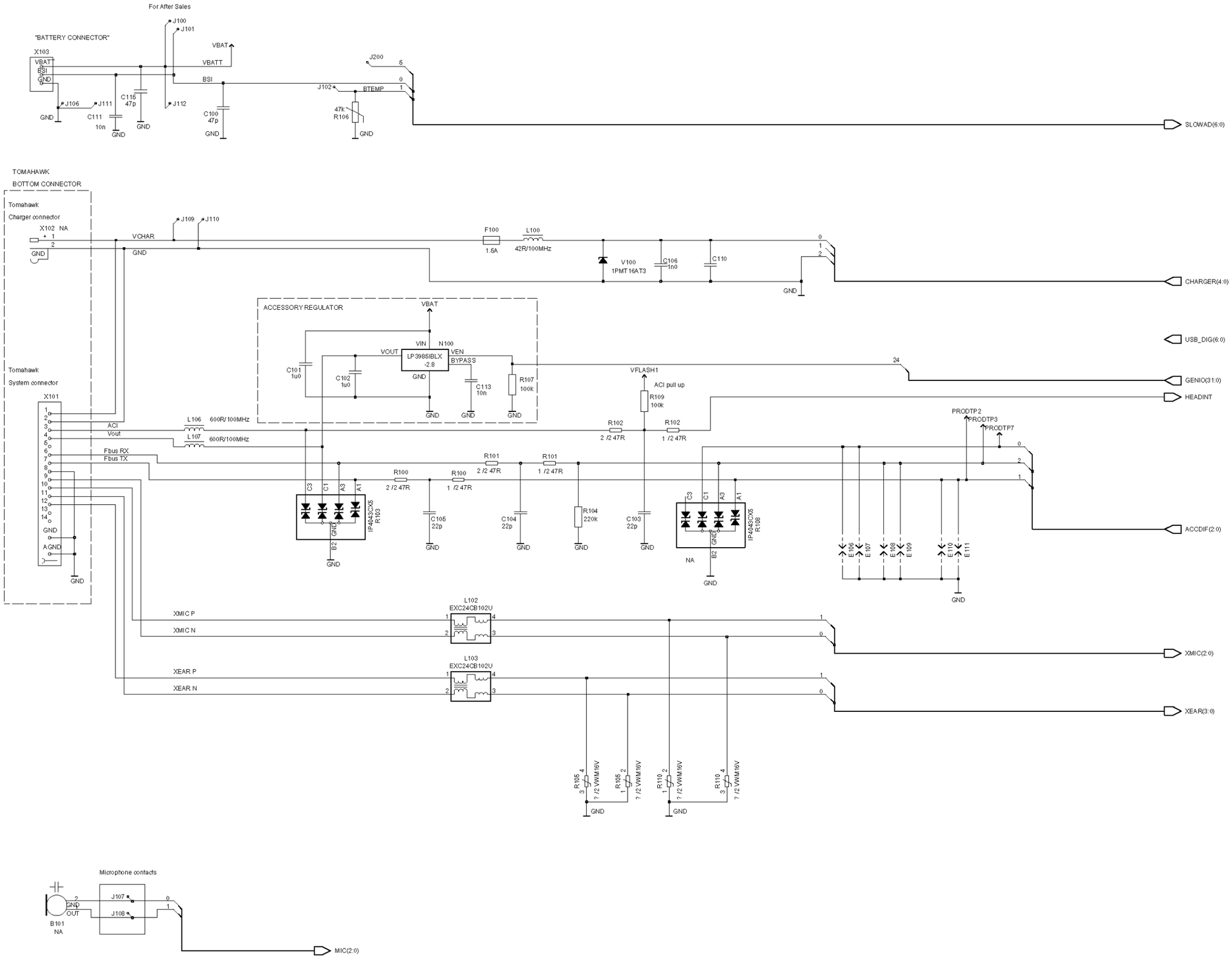
Alfred


VR4
C901
56p
GND
L901
12nH
C903
1p8
Z901
881.5MHz
IN
OUT
GND GND
CEL_LNA_IN
L906
19n0H
N901
ALFRED_CMH0819N
P_MIX_IN
C_MIX_IN
P_LNA_IN
C_LNA_IN
BAND
GAIN_CTL
IFA_IN
IFA_SRC
IF_SEL
LO_IN
AMPS_OUT
MIX_OUT
P_LNA_OUT
C_LNA_OUT
CDMA_OUT
LO_VDD_C
LO_VDD_P
P_RFA_VDD
VDD
RFA_VDD_C
GND
L903
100nH
C906
100p
C907
3p3
L909
150nH
C913
3n3
C914
100n
C912
3p9
R914
0R
IF_CDMA
RX_SW1
R902
47k
C909
100p
L908
18nH
R905
82R
C919
1n0
VCO_RX
R911
18R
R912
270R
R913
270R
L910
68nH
L907
15nH
R910
5R6
C918
47n
C917
27p

DCT4 Common Baseband



System Connector



C-MAX

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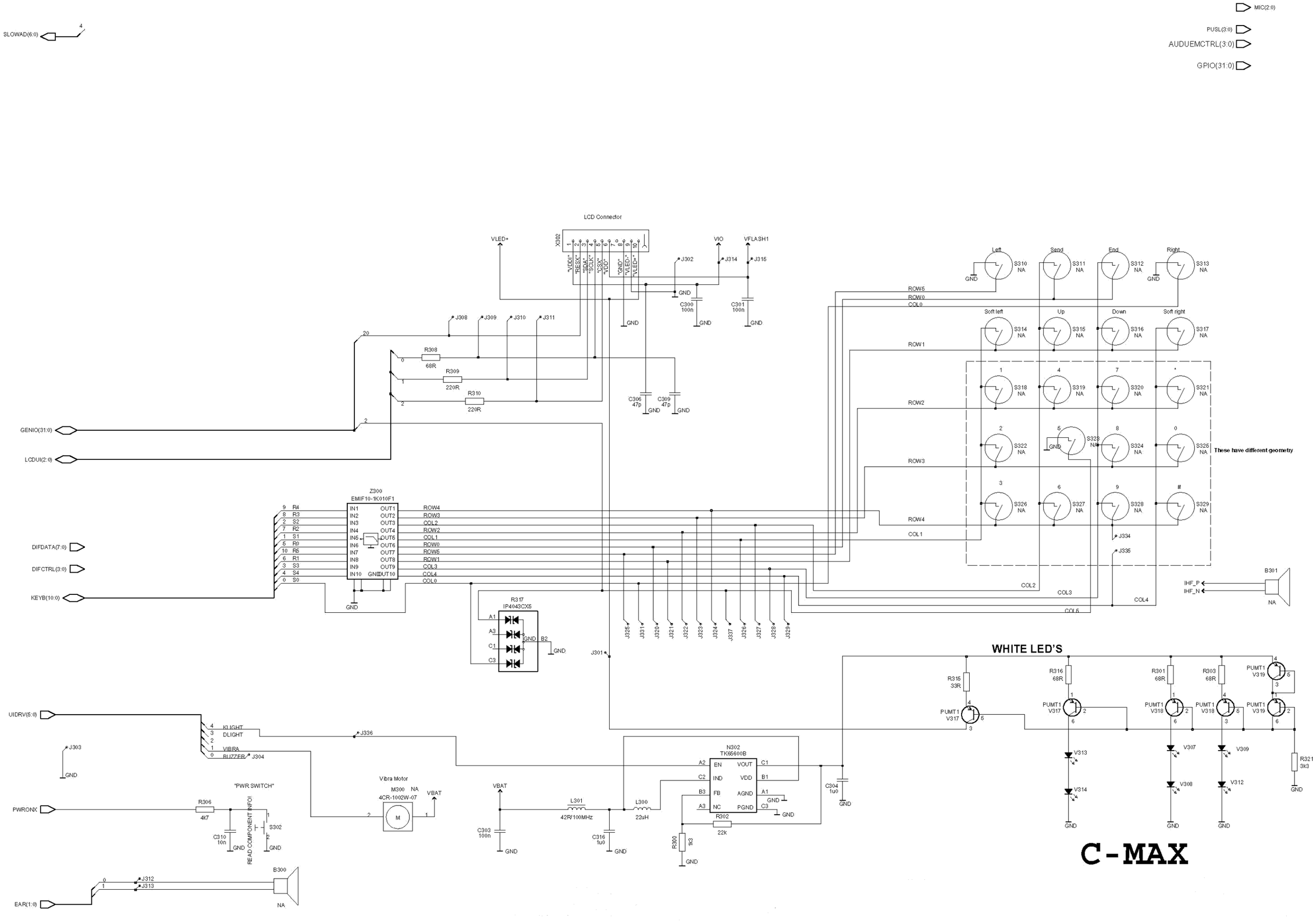
User Interface



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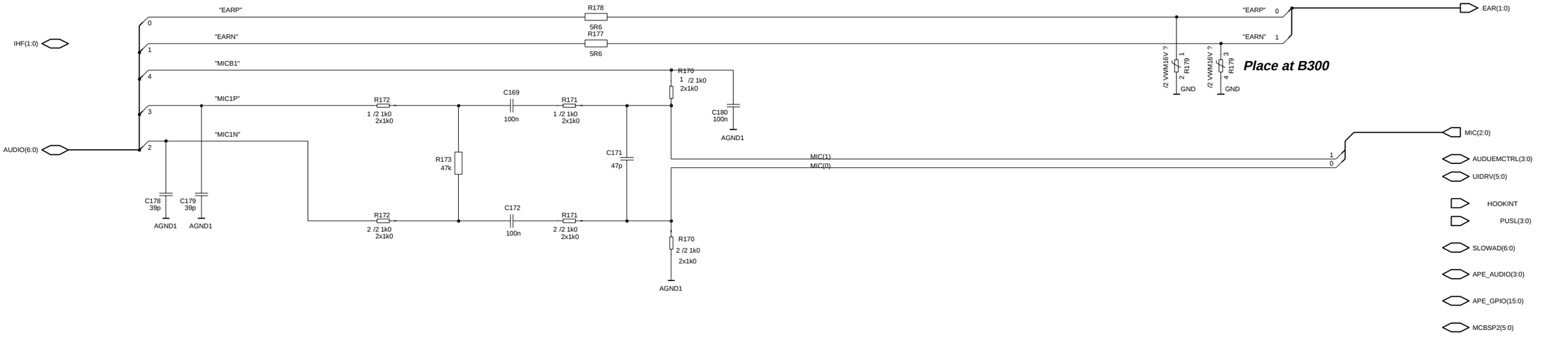
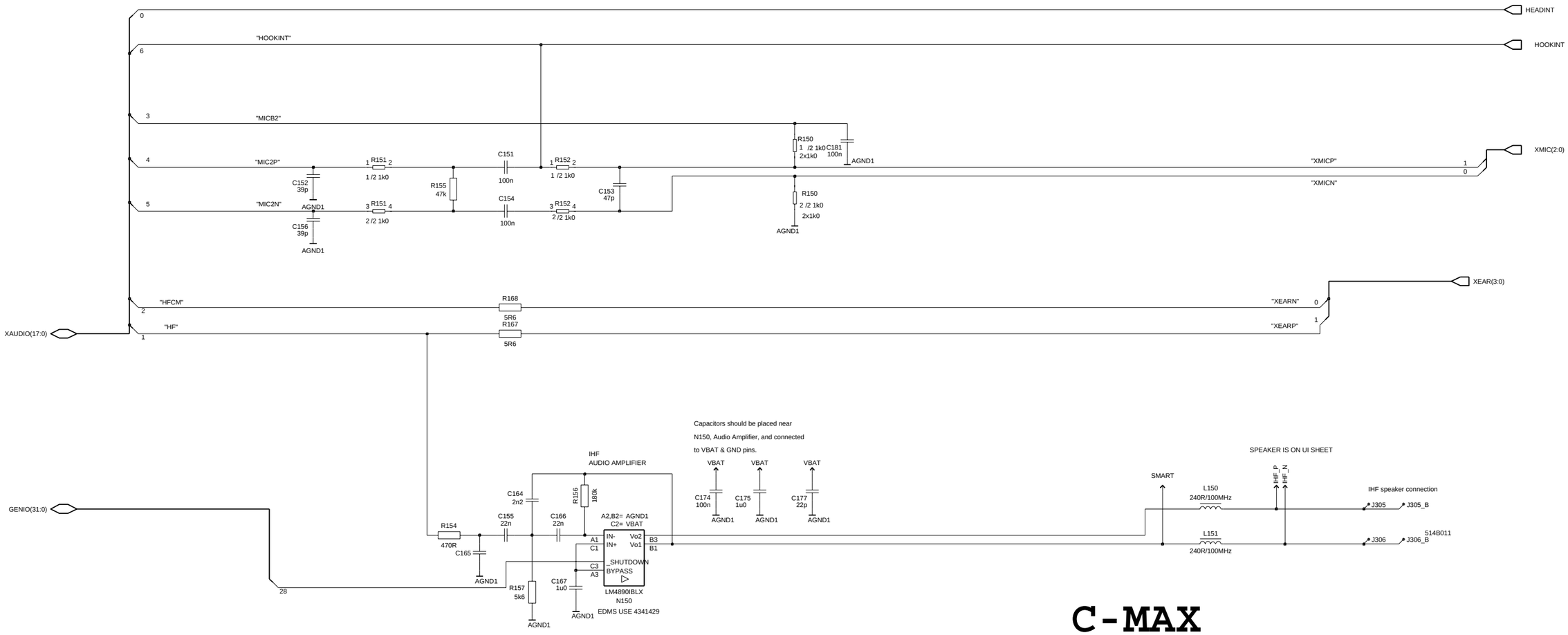
Audio





C-MAX

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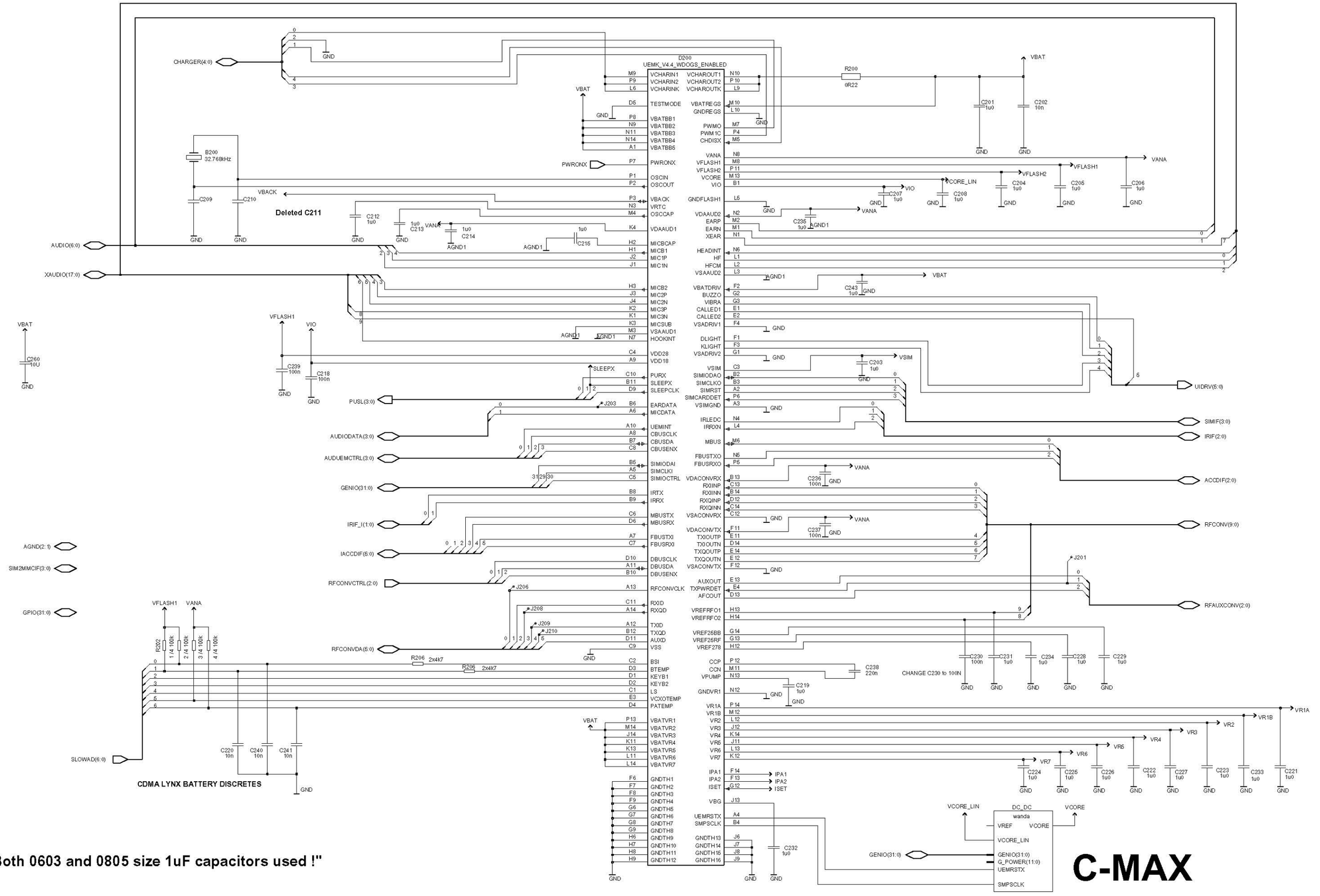
Discrete Power Management



"Both 0603 and 0805 size 1uF capacitors used !"

Used refs in REG_CAP: C238, C200, C219, C233, C221 and R205

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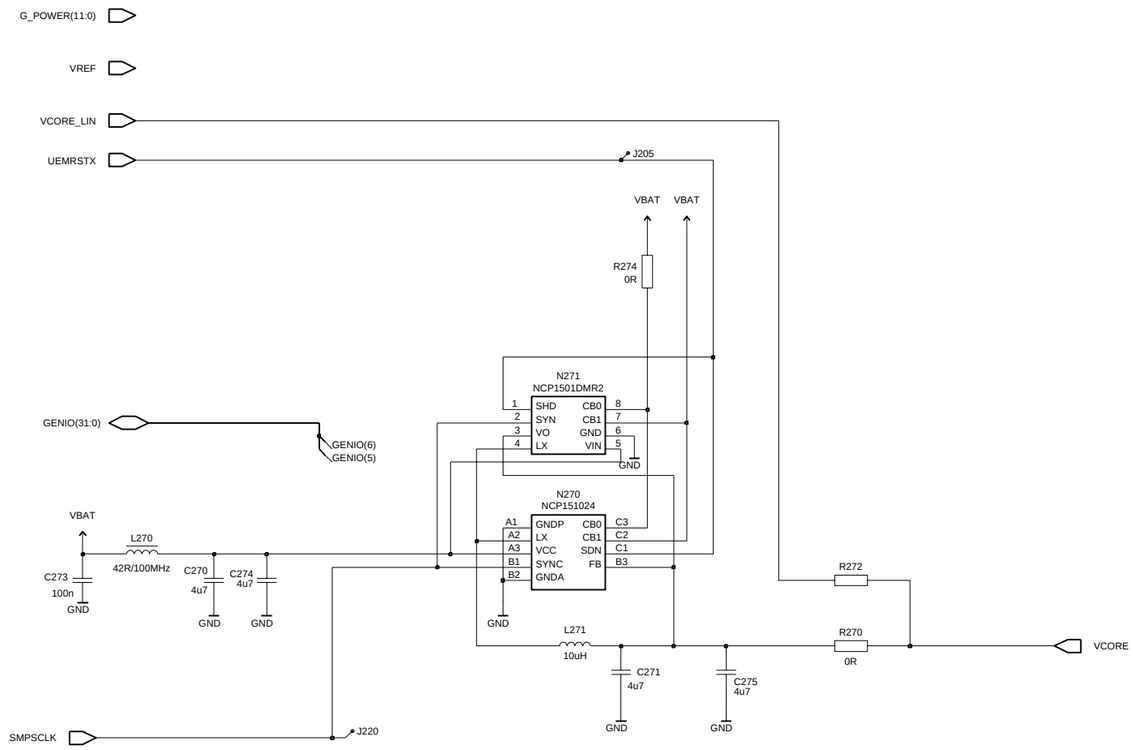
DC/DC Converter



2X 4u7 Caps used for height reasons.

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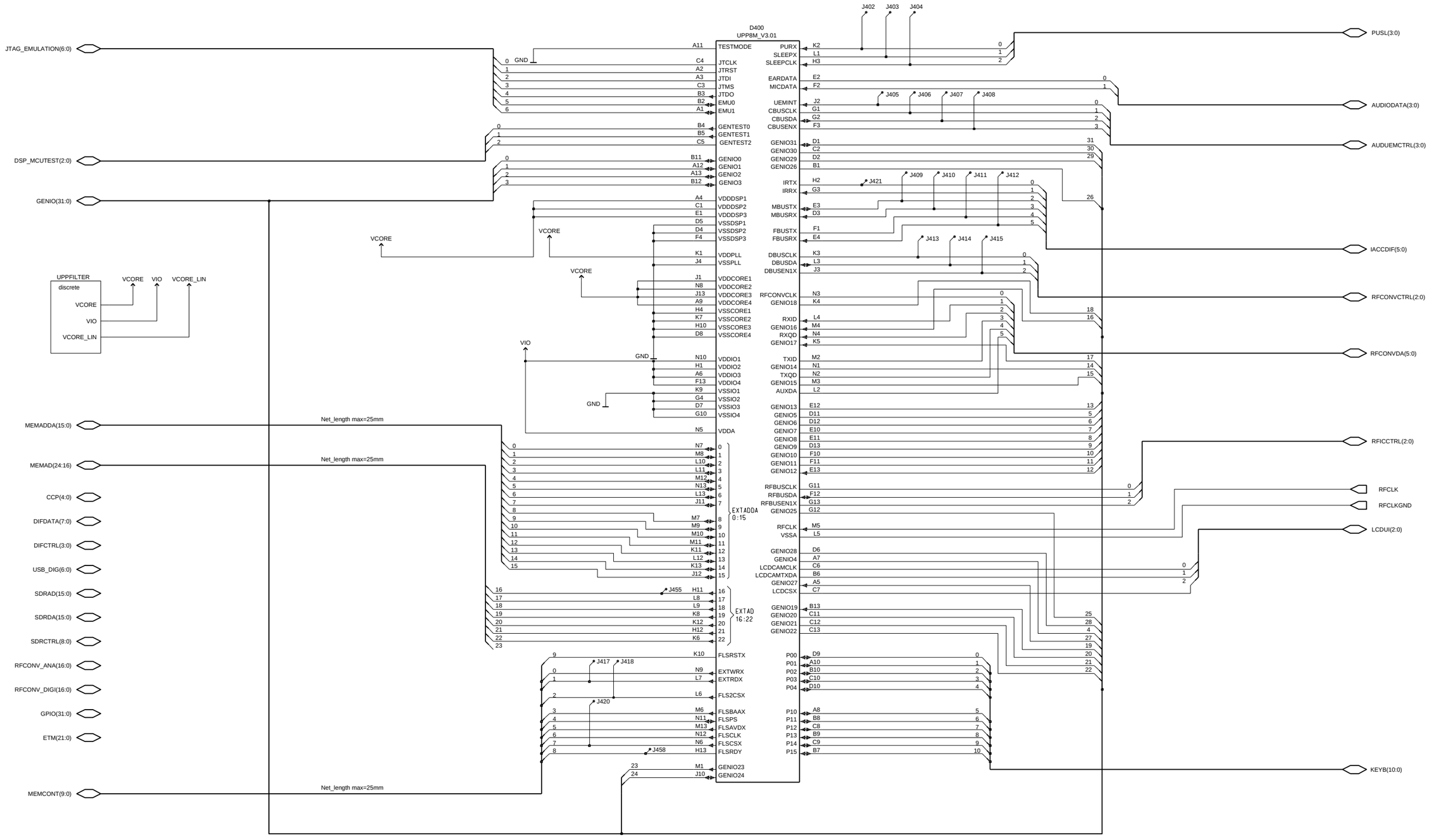
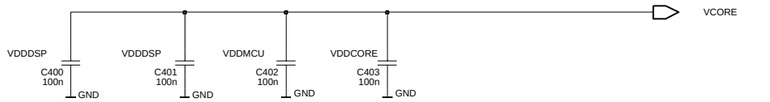
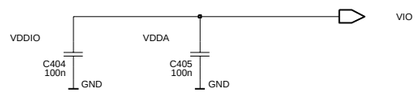
UPP

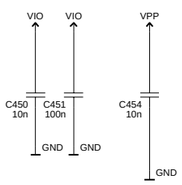

D400
UPP8M_V3.01
JTAG_EMULATION(6:0)
DSP_MCUTEST(2:0)
GENIO(31:0)
UPPFILTER
discrete
VCORE
VIO
VCORE_LIN
MEMADDA(15:0)
MEMAD(24:16)
CCP(4:0)
DIFDATA(7:0)
DIFCTRL(3:0)
USB_DIG(6:0)
SDRAD(15:0)
SDRDA(15:0)
SDRCTRL(8:0)
RFCONV_ANA(16:0)
RFCONV_DIGI(16:0)
GPIO(31:0)
ETM(21:0)
MEMCONT(9:0)
Net_length max=25mm
TESTMODE
JTCLK
JTRST
JTDI
JTMS
JTDO
EMU0
EMU1
GENTEST0
GENTEST1
GENTEST2
GENIO0
GENIO1
GENIO2
GENIO3
VDDDSP1
VDDDSP2
VDDDSP3
VSSDSP1
VSSDSP2
VSSDSP3
VDDPLL
VSSPLL
VDDCORE1
VDDCORE2
VDDCORE3
VDDCORE4
VSSCORE1
VSSCORE2
VSSCORE3
VSSCORE4
VDDIO1
VDDIO2
VDDIO3
VDDIO4
VSSIO1
VSSIO2
VSSIO3
VSSIO4
VDDA
EXTADDA 0:15
EXTAD 16:22
FLSRSTX
EXTWRX
EXTRDX
FLS2CSX
FLSBAAX
FLSPS
FLSAVDX
FLSCLK
FLSCSX
FLSRDY
GENIO23
GENIO24
PURX
SLEEPX
SLEEPCLK
EARDATA
MICDATA
UEMINT
CBUSCLK
CBUSDA
CBUSENX
GENIO31
GENIO30
GENIO29
GENIO26
IRTX
IRRX
MBUSTX
MBUSRX
FBUSTX
FBUSRX
DBUSCLK
DBUSDA
DBUSEN1X
RFCONVCLK
GENIO18
RXID
GENIO16
RXQD
GENIO17
TXID
GENIO14
TXQD
GENIO15
AUXDA
GENIO13
GENIO5
GENIO6
GENIO7
GENIO8
GENIO9
GENIO10
GENIO11
GENIO12
RFBUSCLK
RFBUSDA
RFBUSEN1X
GENIO25
RFCLK
VSSA
GENIO28
GENIO4
LCDCAMCLK
LCDCAMTXDA
GENIO27
LCDCSX
GENIO19
GENIO20
GENIO21
GENIO22
P00
P01
P02
P03
P04
P10
P11
P12
P13
P14
P15
J402
J403
J404
J405
J406
J407
J408
J421
J409
J410
J411
J412
J413
J414
J415
J417
J418
J420
J455
J458
PUSL(3:0)
AUDIODATA(3:0)
AUDUEMCTRL(3:0)
IACCDIF(5:0)
RFCONVCTRL(2:0)
RFCONVDA(5:0)
RFICCTRL(2:0)
RFCLK
RFCLKGND
LCDUI(2:0)
KEYB(10:0)

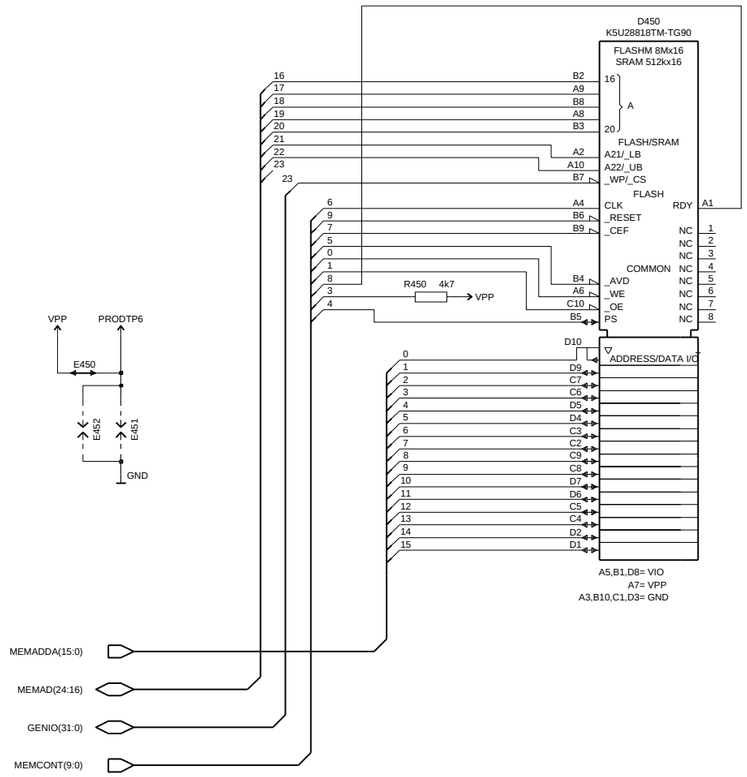
UPP Filter





Combo Memory 128+8 Mbit





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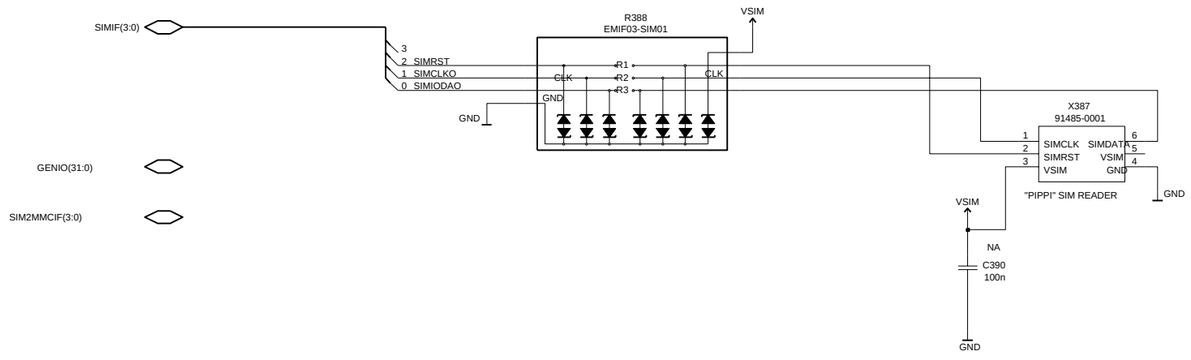
SIM Reader



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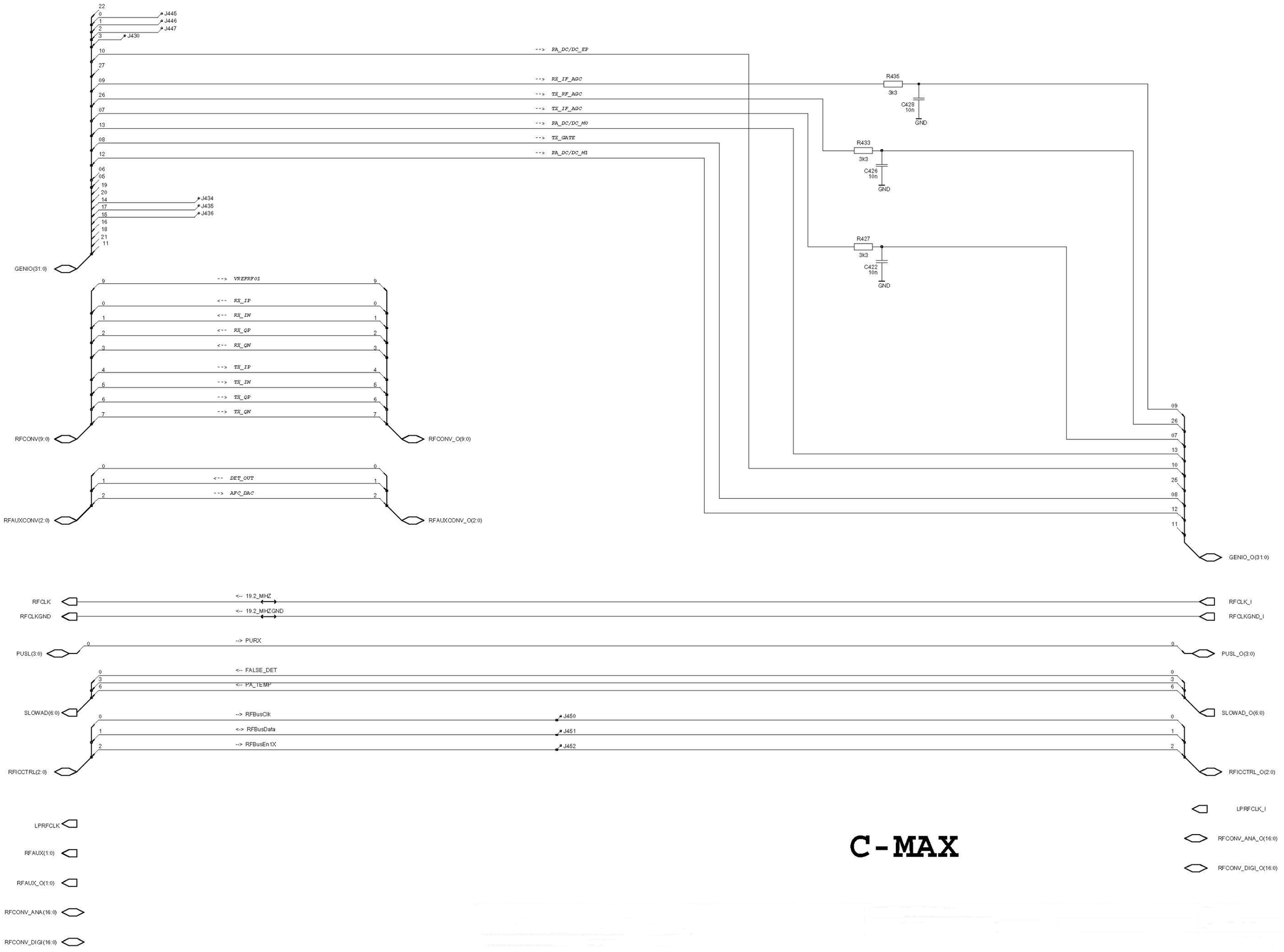
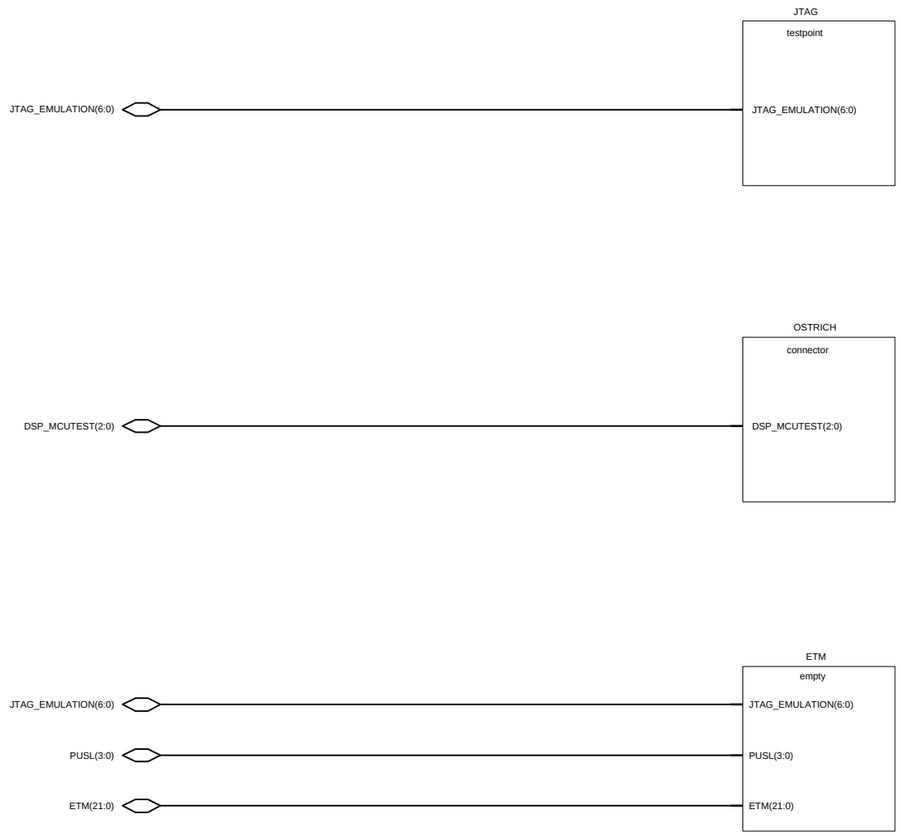
BB-RF Interface



Test and Emulator Interface



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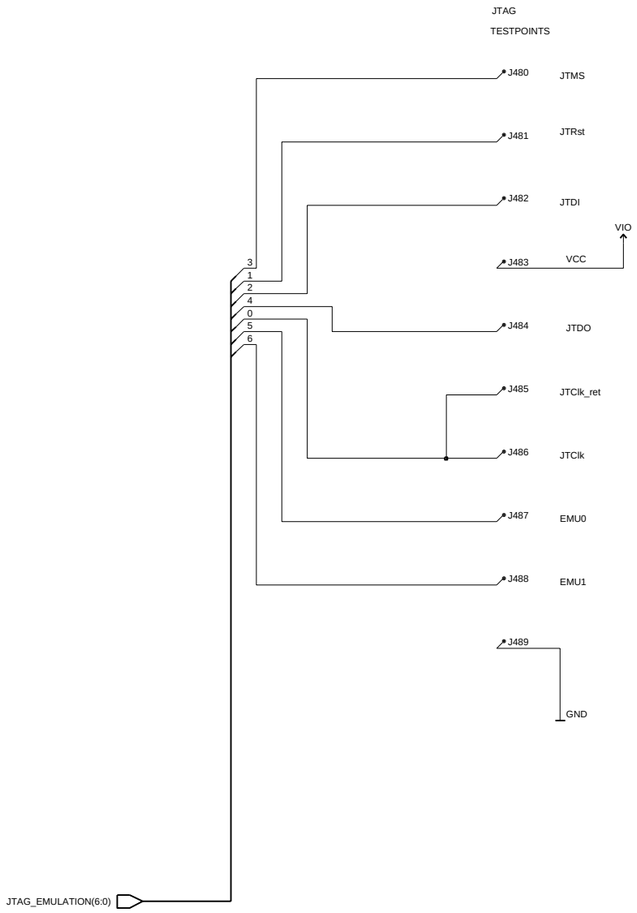
Test Points for JTAG Emulator



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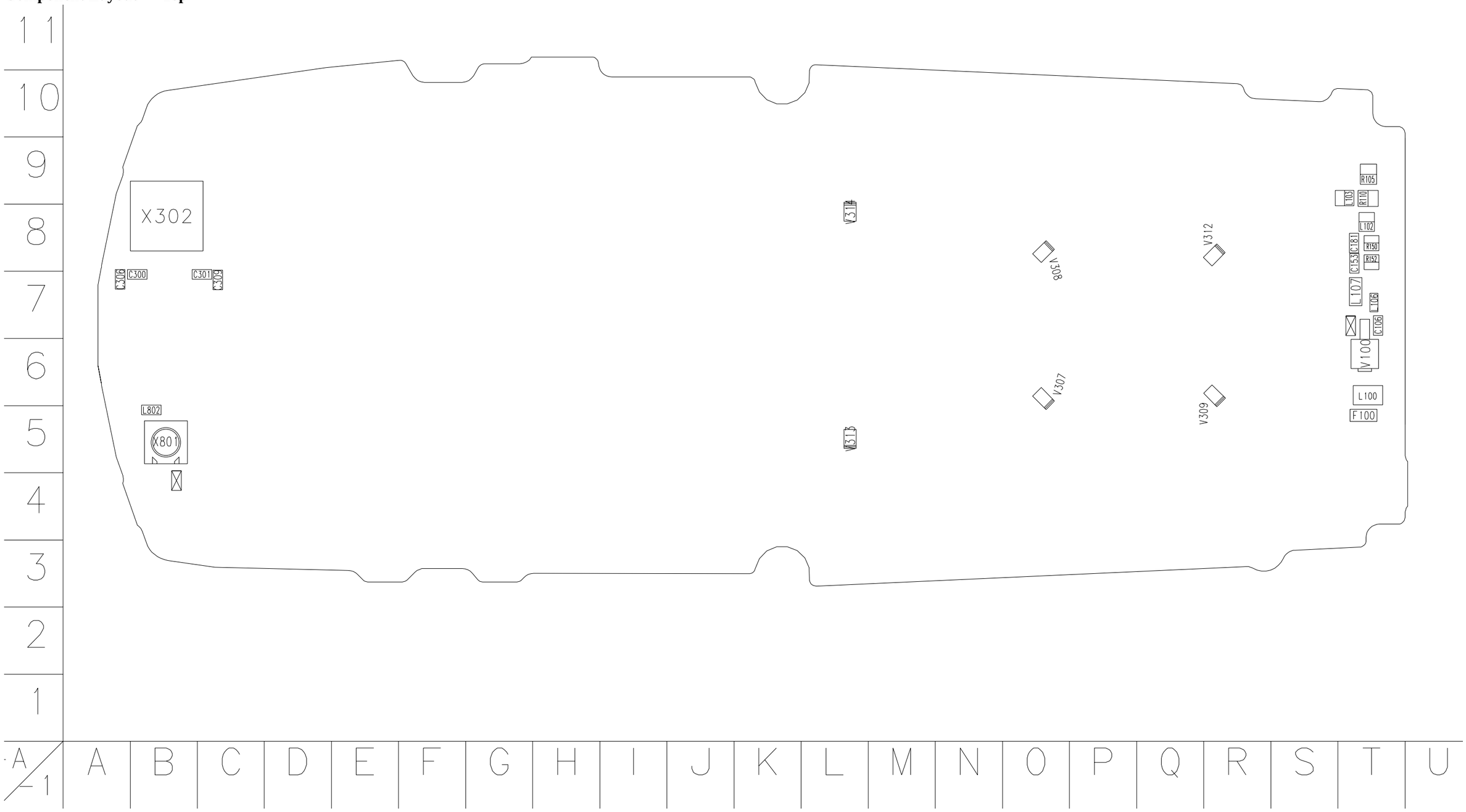
Component Layout — Top



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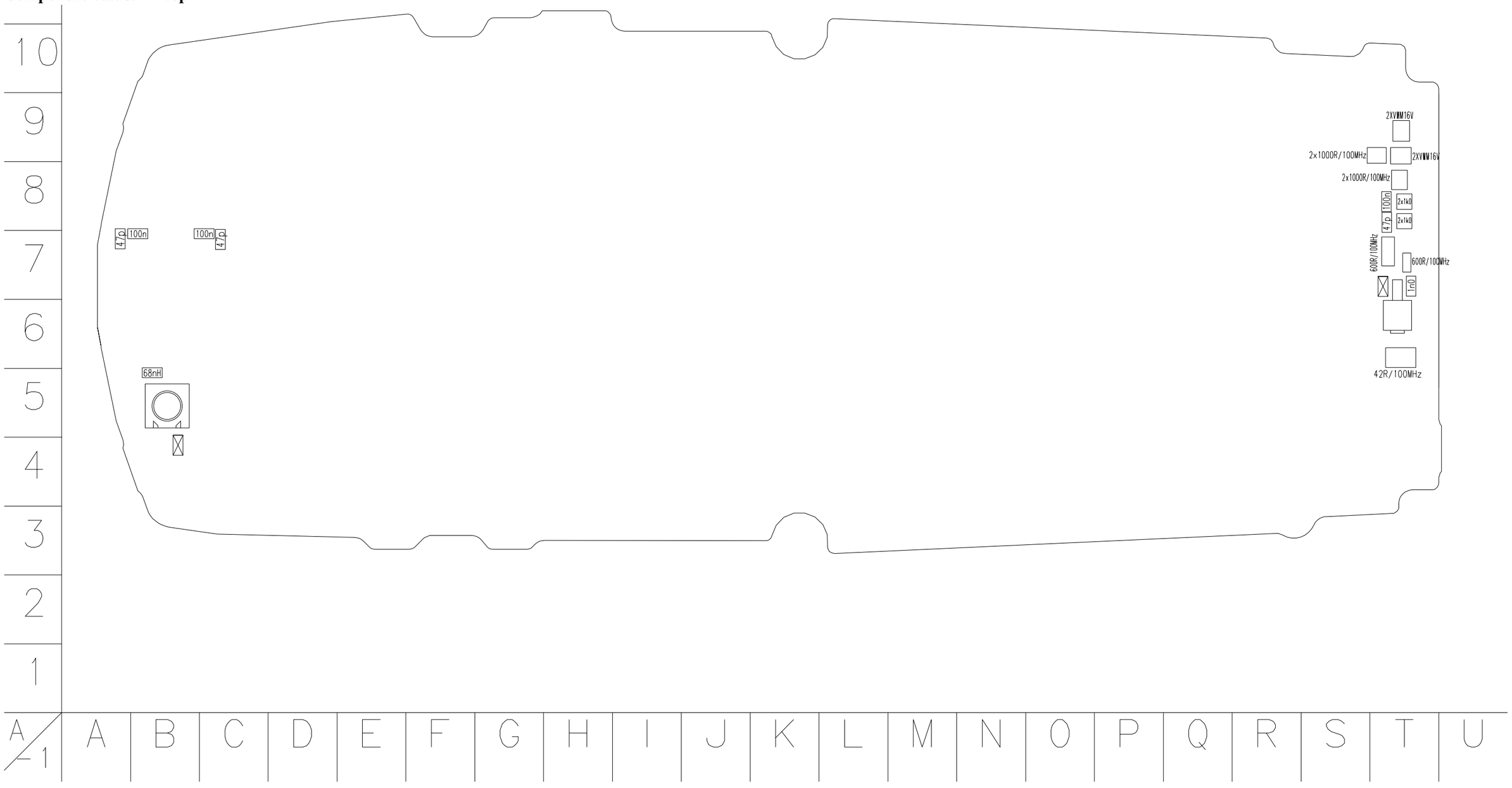
Component Layout — Bottom

Component Values — Top



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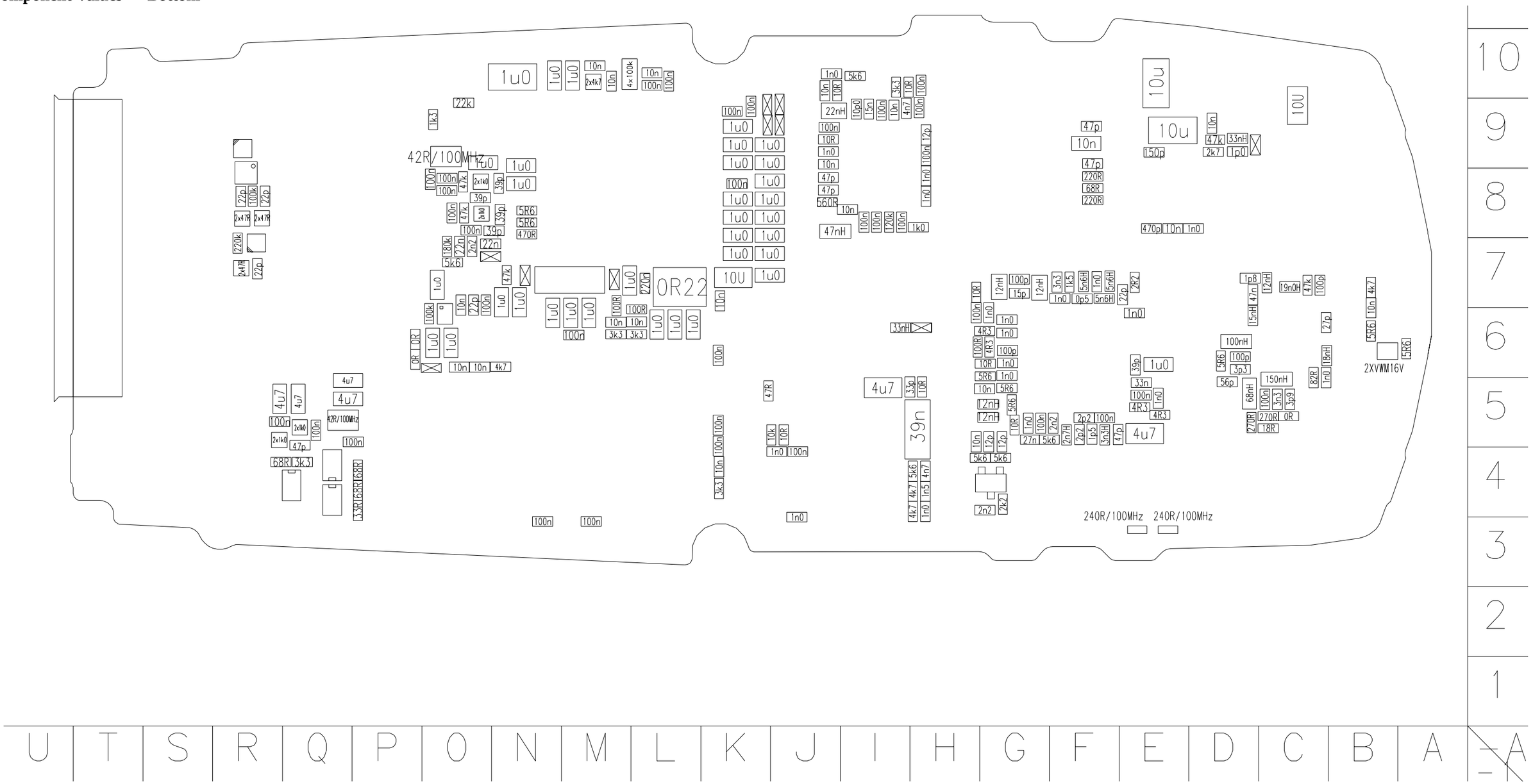
Component Values — Bottom



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